

# Smart IDENTIFICATION

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## Contactless Memories

	my-d™ vicinity				SLE 55Rxx
Product name	SRF 55Vo2P	SRF 55V10P	SRF 55Vo2S	SRF 55V10S	SLE 55R01
<b>Product description</b>	Plain memory, 2.5 kbit EEPROM	Plain memory, 10 kbit EEPROM	Security memory with authentication, 2.5 kbit EEPROM	Security memory with authentication 10 kbit EEPROM	Security memory with authentication, 160 Byte EEPROM
<b>Interface</b>	ISO / IEC 15693	ISO / IEC 15693	ISO / IEC 15693	ISO / IEC 15693	ISO / IEC 14443 (type A)
<b>Memory organization</b>	1 fixed sector	1 fixed sector	Up to 16 sectors fully configurable	Up to 16 sectors fully configurable	Up to 16 sectors fully configurable
<b>Counter</b>	Up to 65,536 units, support of anti-tearing	Up to 65,536 units, support of anti-tearing	Up to 65,536 units, support of anti-tearing	Up to 65,536 units, support of anti-tearing	Up to 65,536 units, support of anti-tearing
<b>Operating frequency</b>	13.56 MHz	13.56 MHz	13.56 MHz	13.56 MHz	13.56 MHz
<b>EEPROM – user</b>	2 kbit	8 kbit	2 kbit	8 kbit	128 Byte
<b>EEPROM – administration</b>	0.5 kbit	2 kbit	0.5 kbit	2 kbit	32 Byte
<b>Security features</b>	Unique serial number, individual page locking	Unique serial number, individual page locking	Transport key, unique serial number, mutual authentication with 64-bit keys, hierarchical key management	Transport key, unique serial number, mutual authentication with 64-bit keys, hierarchical key management	Transport key, unique serial number, mutual authentication with 64-bit keys, hierarchical key management
<b>Distance (read / write)</b>	Typical 0 to 70 cm <sup>1)</sup>	Typical 0 to 70 cm <sup>1)</sup>	Typical 0 to 70 cm <sup>1)</sup>	Typical 0 to 70 cm <sup>1)</sup>	Typical 0 to 10 cm <sup>1)</sup>
<b>Data rate</b>	26 kbit/s	26 kbit/s	26 kbit/s	26 kbit/s	106 kbit/s
<b>Anticollision</b>	Yes	Yes	Yes	Yes	Yes
<b>Ambient temperature</b>	-25 to +85°	-25 to +85°	-25 to +85°	-25 to +85°	-25 to +85°
<b>Endurance</b>	100,000	100,000	100,000	100,000	100,000
<b>Retention time, at least</b>	10 years	10 years	10 years	10 years	10 years
<b>Delivery forms</b>	Inlay 76*45 mm <sup>2</sup> , inlay 45*45 mm <sup>2</sup> , module MCC2, wafer	Inlay 76*45 mm <sup>2</sup> , inlay 45*45 mm <sup>2</sup> , module MCC2, wafer	Inlay 76*45 mm <sup>2</sup> , inlay 45*45 mm <sup>2</sup> , module MCC2, wafer	Inlay 76*45 mm <sup>2</sup> , inlay 45*45 mm <sup>2</sup> , module MCC2, wafer	Module MCC2, wafer
<b>Tools</b>	my-d™ vicinity kit	my-d™ vicinity kit	my-d™ vicinity kit	my-d™ vicinity kit	CR-EVA-kit
<b>Applications</b>	Item management, access control	Item management, access control	Item management, access control, chip sharing approach	Item management, chip sharing approach	Automatic fare collection, electronic purse, access control, identification

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1) depending on antenna configuration

**Standard Mifare® /SLE 66R35**

SLE 55R04	SLE 55R08	SLE 55R16	SLE 44R35S	SLE 66R35
Security memory with authentication, 770 Byte EEPROM	Security memory with authentication, 1,280 Byte EEPROM	Security memory with authentication, 2,560 Byte EEPROM	Intelligent 1 kByte EEPROM	Intelligent 1 kByte EEPROM
ISO / IEC 14443 (type A)	ISO / IEC 14443 (type A)	ISO / IEC 14443 (type A)	ISO / IEC 14443 (type A)	ISO / IEC 14443 (type A)
Up to 16 sectors fully configurable	Up to 16 sectors fully configurable	Up to 16 sectors fully configurable	16 fixed sectors	16 fixed sectors
Up to 65,536 units, support of anti-tearing	Up to 65,536 units, support of anti-tearing	Up to 65,536 units, support of anti-tearing	–	–
13.56 MHz	13.56 MHz	13.56 MHz	13.56 MHz	13.56 MHz
616 Byte	1,024 Byte	2,048 Byte	768 Byte	768 Byte
154 Byte	256 Byte	512 Byte	256 Byte	256 Byte
Transport key, unique serial number, mutual authentication with 64-bit keys, hierarchical key management	Transport key, unique serial number, mutual authentication with 64-bit keys, hierarchical key management	Transport key, unique serial number, mutual authentication with 64-bit keys, hierarchical key management	Transport code, unique serial number, mutual three pass authentication with 48-bit keys	Transport code, unique serial number, mutual three pass authentication with 48-bit keys
Typical 0 to 10 cm <sup>1)</sup>	Typical 0 to 10 cm <sup>1)</sup>	Typical 0 to 10 cm <sup>1)</sup>	Typical 0 to 10 cm <sup>1)</sup>	Typical 0 to 10 cm and more <sup>1)</sup>
106 kbit/s	106 kbit/s	106 kbit/s	106 kbit/s	106 kbit/s
Yes	Yes	Yes	Yes	Yes
–25 to +85°	–25 to +85°	–25 to +85°	–25 to +70°	–25 to +70°
100,000	100,000	100,000	100,000	100,000
10 years	10 years	10 years	10 years	10 years
Module MCC2, wafer	Module MCC2, wafer	Module MCC2, wafer	Module MCC2, wafer, bumped wafer	Module MCC8, MCC2, wafer, bumped wafer
CR-EVA-kit	CR-EVA-kit	CR-EVA-kit	CR-EVA-kit	CR-EVA-kit
Automatic fare collection, electronic purse, access control, identification	Automatic fare collection, electronic purse, access control, identification	Automatic fare collection, electronic purse, access control, identification	Automatic fare collection, electronic purse, access control, identification	Automatic fare collection, electronic purse, access control, identification

## Dual-Interface / Contactless Controllers

### SLE66CLXxxxP

Product name	SLE 66CL80P	SLE 66CL81P	SLE 66CLX320P	SLE 66CLX321P
<b>Product description</b>	Dual interface security controller	Pure contactless security controller	Dual interface security cryptocontroller	Pure contactless security cryptocontroller
<b>User-ROM</b>	72 kByte	72 kByte	136 kByte	136 kByte
<b>EEPROM</b>	8 kByte	8 kByte	32 kByte	32 kByte
<b>RAM</b>	2,304 Byte	2,304 Byte	4,352 + 700 Byte Crypto	4,352 + 700 Byte Crypto
<b>CPU</b>	16 bit	16 bit	16 bit	16 bit
<b>Crypto coprocessor</b>	–	–	1,100 bit modular arithmetic	1,100 bit modular arithmetic
<b>HW-DES</b>	Yes	Yes	Yes	Yes
<b>Clock (int.)</b>	1–15 MHz	1–15 MHz	1–15 MHz	1–15 MHz
<b>Clock (ext.) contact-based</b>	1–5 MHz	–	1–5 MHz	–
<b>Operating voltage</b>	2.7 V–5.5 V	2.7 V–5.5 V	2.7 V–5.5 V	2.7 V–5.5 V
<b>Max. supply current</b>	10 mA @ 15 MHz	10 mA @ 15 MHz	10 mA @ 15 MHz	10 mA @ 15 MHz
<b>Max. sleep mode current (typ.)</b>	100 µA	100 µA	100 µA	100 µA
<b>Temperature range</b>	CB: -25 to +85° CL: 15 to +70°	-15 to +70°	CB: -25 to +85° CL: 15 to +70°	-15 to +70°
<b>Write / erase time (at 5 MHz)</b>	4.5 ms (typ.)	< 4.0 ms (typ.)	4.5 ms (typ.)	< 4.0 ms (typ.)
<b>EEPROM page programming</b>	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte
<b>MMU</b>	Yes	Yes	Yes	Yes
<b>Security features</b>	Tamper-proof design, chip ID counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, triple DES in HW	Tamper-proof design, chip ID counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, triple DES in HW	Tamper-proof design, chip ID counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, triple DES in HW, asymmetric algorithms, hardware-supported (e.g. RSA, ECC)	Tamper-proof design, chip ID counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, triple DES in HW, asymmetric algorithms, hardware-supported (e.g. RSA, ECC)
<b>Peripherals</b>	2 x 16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART, triple DES accelerator	2 x 16-bit autoreload timer, PLL, interrupt, CRC, RNG, triple DES accelerator	2 x 16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART, triple DES accelerator	2 x 16-bit autoreload timer, PLL, interrupt, CRC, RNG, triple DES accelerator
<b>Delivery forms</b>	Module M8.4, bumped wafer, wafer	Contactless module MCC8, wafer	Module M8.4, bumped wafer, wafer	Contactless module MCC8, wafer
<b>Applications</b>	Banking, access control, transport, identification, multifunctional card	Banking, access control, transport, identification	Banking, Pay TV, PKI, security access, multifunctional card, identification, transport	Banking, security access, transport, identification
<b>Certifications</b>	CC EAL 5+ planned, Visa level 3 planned	CC EAL 5+ planned, Visa level 3 planned	CC EAL 5+ planned, Visa level 3 planned	CC EAL 5+ planned, Visa level 3 planned

## Components for Contactless Terminals

### SLF9XXX

Product name	SLF 9000N	SLF 9611
Product description	Contactless codec for proximity terminals	Security access module for SLE 55RXX
Interface	ISO / IEC 14443 (Type A & B)	ISO / IEC 7816
Memory organization	Terminal IC for contactless chip cards with multiplexed / demultiplexed 8-bit parallel interface	Enables high secure authentication between the terminal and the my-d™ chip card and handles cards communication with the background system. Furthermore offers the capability to manage security mechanism with the background system.
Operating frequency	13.56 MHz	–
Security features	Transparent for all data, programmable CRC and parity checks	Evaluation according to ITSEC E3, based on SLE 66CxxxP security controller, my-d™ 64-bit cryptographic algorithm, triple DES in HW for key diversification
Data rate	106 kbit/s	115 kbit/s
Anticollision	ISO / IEC 14443 Type A&B, deterministic and time slot with slot marker capability	–
Ambient temperature	–40 to +85°	–25 to +75°
Delivery forms	44-pin plastic leaded chip carrier (PLCC), LQFP or bare chip optional	ID-000 / ID1
Applications	Digital interface in stationary or handheld contactless reader / writer terminals or terminal modules	Security applications with key-based authentication

## Contact-Based Security Memories

Product name	SLE 4406S Classic	SLE 4436 Eurochip 1	SLE 5536 Eurochip 2	SLE 6636 Eurochip 66	SLE 7736 Eurochip 77	SLE 4432 DataCarrier
<b>Product description</b>	Intelligent 128-bit EEPROM counter with security logic	Intelligent 237-bit EEPROM counter with security logic & high security authentication	Intelligent 237-bit EEPROM counter with security logic & high security authentication	Intelligent 237-bit EEPROM counter with security logic & high security authentication	Intelligent 237-bit EEPROM counter with security logic & high security authentication	Intelligent 256 Byte EEPROM with write-protect function
<b>Counter</b>	>20,000 count units	>20,000 count units, support of anti-tearing	>20,000 count units, support of anti-tearing	>20,000 count units, support of anti-tearing	>20,000 count units, support of anti-tearing	–
<b>ROM</b>	24 bit	24 bit	24 bit	24 bit	24 bit	–
<b>PROM</b>	72 bit	177 bit	177 bit	177 bit	177 bit	32 bit
<b>EEPROM</b>	32 bit	32 bit	32 bit	32 bit	32 bit	256 Byte
<b>Security features</b>	Security logic, irreversible chip coding, transport code	High security authentication with 1 or 2 keys, security logic, irreversible chip coding, transport code	High security authentication with 1 or 2 keys, optional cipher block chaining, security logic, irreversible chip coding, transport code	High security authentication with 1 or 2 keys, optional cipher block chaining, security logic, irreversible chip coding, transport code dedicated CMOS security technology	High security authentication with 1 or 2 keys, optional cipher block chaining, security logic, irreversible chip coding, transport code, dedicated CMOS security technology	Byte protection, irreversible chip coding
<b>Min. write / erase time</b>	3 ms	3 ms	3 ms	3 ms	3 ms	2.5 ms
<b>Operating voltage</b>	5 V	5 V	5 V	5 V	3–5 V	5 V
<b>Max. supply current</b>	1 mA	5 mA	5 mA	1 mA (typ. 400 µA)	1 mA (typ. 300 µA/5 V VDD)	10 mA
<b>Ambient temperature</b>	–40 to +80°	–35 to +80°	–35 to +80°	–40 to +80°	–40 to +80°	–35 to +80°
<b>Endurance</b>	100,000	100,000	100,000	100,000	100,000	100,000
<b>Retention time, at least</b>	30 years	10 years	10 years	30 years	30 years	10 years
<b>Delivery forms</b>	Module M3, die	Module M3, die	Module M3, die	Module M3, die	Module M3, die	Module M3, die
<b>Tools</b>	EVA-kit	EVA-kit	EVA-kit	EVA-kit	EVA-kit	EVA-kit
<b>Applications</b>	Prepaid phone card	Prepaid phone card, vending	Prepaid phone card, vending	Prepaid phone card, vending	Prepaid phone card, vending	Health insurance, member card, electronic ticketing, loyalty

<b>SLE 4442 DataCarrier</b>	<b>SLE 4418 DataCarrier</b>	<b>SLE 4428 DataCarrier</b>
Intelligent 256 Byte EEPROM with write-protect function & security logic	Intelligent 1 kByte EEPROM with write-protect function	Intelligent 1 kByte EEPROM with write-protect function & security logic
–	–	–
–	–	–
32 bit	1,024 bit	1,024 bit
256 Byte	1,024 Byte	1,024 Byte
security code, Byte protection, irreversible chip coding, transport code	Byte protection, irreversible chip coding	Security code, Byte protection, irreversible chip coding, transport code
2.5 ms	5 ms	5 ms
5 V	5 V	5 V
10 mA	10 mA	10 mA
–35 to +80°	–35 to +100°	–35 to +100°
100,000	100,000	100,000
10 years	10 years	10 years
Module M3, die	Module M2, die	Module M2, die
EVA-kit	EVA-kit	EVA-kit
Health insurance, member card, electronic ticketing, loyalty	Health insurance, member card, electronic ticketing, loyalty	Health insurance, member card, electronic ticketing, loyalty

## Security Controller Overview in EEPROM Sizes

EEPROM	0.5 KB	2 KB	4 KB			8 KB
Product name	SLE 22C05S	SLE 44C20S	SLE 66C40S	SLE 66C42P	SLE 66C44PE	SLE 44C80S
Product description	Security controller	Security controller	Security controller	Security controller	Security controller	Security controller
User-ROM	7.5 kByte	15 kByte	31.5 kByte	62 kByte	68 kByte	15 kByte
EEPROM	512 Byte	2 kByte	4 kByte	4 kByte	4 kByte	8 kByte
RAM	128 Byte	256 Byte	1,280 Byte	2,304 Byte	2,304 Byte	256 Byte
CPU	16 bit	8 bit	16 bit	16 bit	8/16 bit	8 bit
Crypto coprocessor	–	–	–	–	–	–
Clock (int.)	1–5 MHz	1–5 MHz	1–5 MHz	1–15 MHz	1–33 MHz	1–5 MHz
Clock (ext.)	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz
Operating voltage	2.7 V–5.5 V	2.7 V–5.5 V	2.7 V–5.5 V	2.7 V–5.5 V	1.8 V, 3.0 V, 5.0 V	2.7 V–5.5 V
Max. supply current (at 5 MHz, 5 V)	6 mA (3.0 mA typical)	10 mA (3.2 mA typical)	10 mA (3.2 mA typical)	10 mA (3.2 mA typical)	10 mA	10 mA (3.2 mA typical)
Max. sleep mode current (typical)	100 µA (20 µA)	100 µA (26 µA)	100 µA (26 µA)	100 µA	100 µA	100 µA (26 µA)
Ambient temperature	–25 to +85°	–25 to +85°	–25 to +85°	–25 to +85°	–25 to +70 (85°)	–25 to +85°
Write / erase time (at 5 MHz)	3.6 ms / 1.8 ms	3.6 ms / 1.8 ms	3.6 ms / 1.8 ms	4.1 ms (typ.)	< 2.9 ms	3.6 ms / 1.8 ms
EEPROM page programming	1 to 8 Byte	1 to 8 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 32 Byte
MMU	No	No	No	Yes	Yes	No
Security features	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency sensor, symmetric algorithms (e.g. DES)	Tamper-proof design, chip ID, counter measures against reverse engineering, sensor concept: voltage-, frequency sensor, symmetric algorithms (e.g. DES)	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency sensor, symmetric algorithms (e.g. DES)	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, symmetric algorithms (e.g. DES / AES), triple DES in HW, elliptic curves in HW	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing-attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature sensors, life test function for sensors, symmetric algorithms (e.g. DES / AES)	Tamper-proof design, chip ID, counter measures against reverse engineering, sensor concept: voltage-, frequency sensor, symmetric algorithms (e.g. DES) light-, temperature-, glitch sensor, active shield, symmetric algorithms (e.g. DES / AES), triple DES in HW, elliptic curves in HW
Peripherals	CRC	–	16-bit timer, CRC, RNG, interrupt	16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART	CRC, UART, RNT, 2 x 16-bit timers, interrupt, DES	–
Delivery forms	Module M2, die	Module M5, die	Module M5, die	Module M5, die	Module M5, die	Module M5, die
Applications	Loyalty, gambling, membership, metering, healthcare, banking low level, identification, transport	Loyalty, gambling, membership, metering, healthcare, banking low level, identification, transport	Loyalty, gambling, membership, metering, healthcare, banking low level, identification, transport	Banking (debit – credit), E-Purse, Pay-TV, security access, healthcare	Payment, GSM, Pay-TV, security access, identification	Loyalty, gambling, membership, banking low level, healthcare, R-UIM, identification, transport, basic GSM, metering
Certifications	–	Visa level 2	Visa level 3, CAST	CC EAL 5+ planned, Visa level 3 planned, CAST	Planned: CC EAL 5 +, Visa, CAST, Proton	Visa level 2

<b>16 KB</b>				
<b>SLE 66C82P</b>	<b>SLE 66C84PE</b>	<b>SLE 66CX80PE</b>	<b>SLE 11C001S</b>	<b>SLE 66C162P</b>
Security controller	Security controller	Security cryptocontroller	Security controller	Security controller
62 kByte	68 kByte	96 kByte	31.5 kByte	70 kByte
8 kByte	8 kByte	8 kByte	16 kByte	16 kByte
2,304 Byte	2,304 Byte	3,004 Byte	1,280 Byte	2,304 Byte
16 bit	8/16 bit	8/16 bit	16 bit	16 bit
–	–	Yes	–	–
1–15 MHz	1–33 MHz	1–33 MHz	1–5 MHz	1–10 MHz
1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz
2.7 V–5.5 V	1.8 V, 3.0 V, 5.0 V	1.8 V, 3.0 V, 5.0 V	2.7 V–5.5 V	2.7 V–5.5 V
10 mA @ 15 MHz	10 mA	10 mA	6 mA (3.0 mA typical)	10 mA @ 10 MHz
100 µA	100 µA	100 µA	100 µA (20 µA)	100 µA
–25 to +85°	–25 to +70 (85°)	–25 to +70 (85°)	–25 to +85°	–25 to +85°
4.1 ms (typ.)	< 2.9 ms	< 2.9 ms	3.6 ms / 1.8 ms	4.5 ms (typ.)
1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte
Yes	Yes	Yes	No	Yes
Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, sensor, symmetric algorithms (e.g. DES)	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing-attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature sensors, life test function for sensors symmetric algorithms (e.g. DES / AES)	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing-attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature sensors, life test function for sensors, symmetric algorithms (e.g. DES / AES) triple DES in HW, elliptic curves in HW	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, Sensor concept: voltage-, frequency-, glitch sensor, symmetric algorithms (e.g. DES)	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency sensor, symmetric algorithms (e.g. DES / AES)
16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART	CRC, UART, RNG, 2 x 16-bit timers, interrupt, DES	CRC, UART, RNG, 2 x 16-bit timers, DES, ACE	CRC	16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART
Module M5, die	Module M5, die	Module M5, die	Module M5, die	Module M5, die
Banking (debit – credit), E-Purse, Pay-TV, security access, GSM, USIM, Ext. healthcare	Payment, GSM, Pay-TV, security access, identification	Payment, GSM, Pay-TV, security access, identification, digital signature	Loyalty, gambling, membership, metering, banking low level, healthcare, basic GSM, R-UIM, identification, transport	Banking (debit – credit), E-Purse, security access, GSM, healthcare, multifunctional card, (SIM Tool Kit) RUIIM
CC EAL 5+ planned, Visa level 3, CAST	Planned: CC EAL 5 +, Visa, CAST, Proton	Planned: CC EAL 5 +, Visa, CAST, Proton, MultOS	–	Visa level 3, CAST

## Security Controller Overview in EEPROM Sizes

	16 KB					
Product name	SLE 66C164P	SLE 66CX160P	SLE 66C166PE	SLE 66C168PE	SLE 66CX162PE	SLE 66C161PE
<b>Product description</b>	Security controller	Security cryptocontroller	Security controller	Security controller	Security cryptocontroller	Security controller
<b>User-ROM</b>	62 kByte	62 kByte	96 kByte	68 kByte	96 kByte	48 kByte
<b>EEPROM</b>	16 kByte	16 kByte	16 kByte	16 kByte	16 kByte	16 kByte
<b>RAM</b>	2,304 Byte	2,304 + 700 Byte Crypto	2,304 Byte	2,304 Byte	3,004 Byte	2,304 Byte
<b>CPU</b>	16 bit	16 bit	8/16 bit	8/16 bit	8/16 bit	8/16 bit
<b>Crypto coprocessor</b>	–	1,100 bit arithmetic	–	–	Yes	–
<b>Clock (int.)</b>	1–10 MHz	1–10 MHz	1–33 MHz	1–33 MHz	1–33 MHz	1–33 MHz
<b>Clock (ext.)</b>	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz
<b>Operating voltage</b>	2.7 V–5.5 V	2.7 V–5.5 V	1.8 V, 3.0 V, 5.0 V	1.8 V, 3.0 V, 5.0 V	1.8 V, 3.0 V, 5.0 V	1.8 V, 3.0 V, 5.0 V
<b>Max. supply current (at 5 MHz, 5 V)</b>	10 mA @ 10 MHz	10 mA @ 10 MHz	10 mA	10 mA	10 mA	10 mA
<b>Max. sleep mode current (typical)</b>	100 µA	100 µA	100 µA	100 µA	100 µA	100 µA
<b>Ambient temperature</b>	–25 to +85°	–25 to +85°	–25 to +70° (85°)	–25 to +70° (85°)	–25 to +70° (85°)	–25 to +70° (85°)
<b>Write / erase time (at 5 MHz)</b>	4.5 ms (typ.)	4.5 ms (typ.)	<2.9 ms (typ.)	<2.9 ms (typ.)	<2.9 ms (typ.)	<2.9 ms (typ.)
<b>EEPROM page programming</b>	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte
<b>MMU</b>	Yes	Yes	Yes	Yes	Yes	Yes
<b>Security features</b>	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, symmetric algorithms (e.g. DES / AES), triple DES in HW, elliptic curves in HW	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, triple DES in HW, elliptic curves in HW, asymmetric algorithms, hardware-supported (e.g. RSA)	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing-attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature sensors, life test function for sensors, symmetric algorithms (e.g. DES / AES)	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing-attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature sensors, life test function for sensors, symmetric algorithms (e.g. DES / AES), elliptic curves in HW	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing-attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature sensors, life test function for sensors, symmetric algorithms (e.g. DES / AES) triple DES in HW	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing-attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature sensors, life test function for sensors, symmetric algorithms (e.g. DES / AES) triple DES in HW
<b>Peripherals</b>	2 x 16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART, triple DES accelerator	2 x 16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART, triple DES accelerator	CDRC, UART, RNG, 2 x 16 bit timer, interrupt, DES	CDRC, UART, RNG, 2 x 16 bit timer, interrupt, DES	CDRC, UART, RNG, 2 x 16 bit timer, interrupt, DES	CDRC, UART, RNG, 2 x 16 bit timer, interrupt, DES
<b>Delivery forms</b>	Module M5, die	Module M5, die	Module M5, die	Module M5, die	Module M5, die	Module M5, die
<b>Applications</b>	Banking (debit – credit), E-Purse, Pay-TV, M / E-commerce, security access, GSM, USIM, Ext. healthcare, multifunctional card	Banking (e-purse), Pay-TV, PKI, security access, GSM, R-UIM, multifunctional card	Payment, GSM, Pay-TV, security access, identification, digital signatures	Payment, GSM, security access, identification	Payment, GSM, Pay-TV, security access, identification, digital signatures	GSM
<b>Certifications</b>	VISA Java, Proton Prisma, ITSEC E4 high, CAST	VISA Java, ZKA, Proton Prisma, ITSEC E4 high, MultOS V4/5, Mondex, CAST	Planned: CC EAL 5+, Visa, CAST, Proton, MultOS	Planned: CC EAL 5+, Visa, Proton, CAST	Planned: CC EAL 5+, Visa, Proton, MultOS, CAST	–

32 KB						64 KB
SLE 66C322P	SLE 66C360PE	SLE 66CX322P	SLE 66CX360PE	SLE 66C324PE	SLE 66C321PE	SLE 66CX642P
Security controller	Security controller	Security cryptocontroller	Security cryptocontroller	Security controller	Security controller	Security cryptocontroller
134 kByte	196 kByte	134 kByte	246 kByte	136 kByte	96 kByte	206 kByte
32 kByte	36 kByte	32 kByte	36 kByte	32 kByte	32 kByte	64 kByte
4,352 Byte	4,352 Byte	4,352 + 700 Byte Crypto	7,100 Byte	2,304 Byte	2,304 Byte	4,352 + 700 Byte Crypto
16 bit	8/16 bit	16 bit	8/16 bit	8/16 bit	8/16 bit	16 bit
–	–	1,100 bit arithmetic	–	–	–	1,100 bit arithmetic
1–10 MHz	1–33 MHz	1–15 MHz	1–33 MHz	1–33 MHz	1–33 MHz	1–15 MHz
1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz
2.7 V–5.5 V	1.8 V, 3.0 V, 5.0 V	2.7 V–5.5 V	1.8 V, 3.0 V, 5.0 V	1.8 V, 3.0 V, 5.0 V	1.8 V, 3.0 V, 5.0 V	1.62 V–5.5 V
10 mA @ 10 MHz	10 mA	10 mA @ 15 MHz	10 mA	10 mA	10 mA	10 mA @ 15 MHz
100 µA	100 µA	100 µA	100 µA	100 µA	100 µA	100 µA
–25 to +85°	–25 to +70° (85°)	–25 to +85°	–25 to +70° (85°)	–25 to +70° (85°)	–25 to +70° (85°)	–25 to +85°
4.5 ms (typ.)	<2.9 ms (typ.)	4.1 ms (typ.)	< 2.9 ms (typ.)	< 2.9 ms (typ.)	< 2.9 ms (typ.)	4.1 ms (typ.)
1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte
Yes	Yes	Yes	Yes	Yes	Yes	Yes
Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, glitch sensor, symmetric algorithms (e.g. DES / AES), triple DES in HW, elliptic curves in HW, hardware-supported (e.g. RSA)	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing-attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature sensors, life test function for sensors, symmetric algorithms (e.g. DES / AES)	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, triple DES in HW, elliptic curves in HW, asymmetric algorithms, hardware-supported (e.g. RSA), elliptic curves in HW	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing-attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature sensors, life test function for sensors, symmetric algorithms (e.g. DES / AES), triple DES in HW	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing-attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature sensors, life test function for sensors, symmetric algorithms (e.g. DES / AES)	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing-attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature sensors, life test function for sensors	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, triple DES in HW, elliptic curves in HW, asymmetric algorithms
2 x 16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART	2 x 16-bit autoreload timer, DES, interrupt, CRC, RNG, UART	2 x 16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART, triple DES accelerator	2 x 16-bit autoreload timer, interrupt, CRC, RNG, UART, ACE, DES	CDRC, UART, RNG, 2 x 16 bit timer, interrupt, DES	CDRC, UART, RNG, 2 x 16 bit timer, interrupt, DES	2 x 16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART, triple DES accelerator
Module M5, die	Module M5, die	Module M5, die	Module M5, die	Module M5, die	Module M5, die	Module M5, die
Loyalty, gambling, membership, metering, banking low level, healthcare, basic GSM, R-UIM, identification, transport	GSM, security access, identification, multi-application card based on MultOS, Java or WinSC	Java-based card, banking (e-purse), Pay-TV, PKI, security access, GSM / USIM, R-UIM, multifunctional card	Banking (e-purse), Pay-TV, security access, GSM / USIM, R-UIM, identification, digital signature	GSM, security access, identification, multiapplication card, security access, Java or WinSC	GSM	Java-based card, banking (e-purse), Pay-TV, PKI, security access, GSM / USIM, R-UIM, multifunctional card
–	CC EAL 5+, Proton Prisma, ZKA, Visa Java, CAST	CC EAL 5+, Proton Prisma, ZKA, Visa Java, CAST	CC EAL 5+, Proton Prisma, ZKA, Visa Java, CAST	Planned: CC EAL 5+, Visa, CAST, Proton, MultOS, ZKA	–	CC EAL 5+ planned, CAST

SLE 66C644P	SLE 66C640P	SLE 66C680PE	SLE 66C682PE	SLE 66C681PE	SLE 66CX680PE
Security controller	Security controller	Security controller	Security controller	Security controller	Security cryptocontroller
206 kByte	134 kByte	246 kByte	196 kByte	136 kByte	246 kByte
64 kByte	64 kByte	68 kByte	68 kByte	68 kByte	68 kByte
4,352 Byte	4,352 Byte	6,400 Byte	4,352 Byte	4,352 Byte	7,100 Byte
16 bit	16 bit	8/16 bit	8/16 bit	8/16 bit	8 / 16 bit
–	–	–	–	–	–
1–15 MHz	1–10 MHz	1–33 MHz	1–33 MHz	1–33 MHz	1–33 MHz
1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5 MHz	1–7.5MHz
1.62 V–5.5 V	2.7 V–5.5 V	1.8 V, 3.0 V, 5.0 V	1.8 V, 3.0 V, 5.0 V	1.8 V, 3.0 V, 5.0 V	1.8 V, 3.0 V, 5.0 V
10 mA @ 15 MHz	10 mA @ 10 MHz	10 mA	10 mA	10 mA	10 mA
100 µA	100 µA	100 µA	100 µA	100 µA	100 µA
–25 to +85°	–25 to +85°	–25 to +70° (85°)	–25 to +70° (85°)	–25 to +70° (85°)	–25 to +70° (85°)
4.1 ms (typ.)	4.5 ms (typ.)	< 2.9 ms	< 2.9 ms	< 2.9 ms	< 2.9 ms
1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte	1 to 64 Byte
Yes	Yes	Yes	Yes	Yes	Yes
Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, symmetric algorithms (e.g. DES / AES), DES accelerator in HW, elliptic curves in HW	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency sensor, symmetric algorithms (e.g. DES / AES), triple DES in HW, elliptic curves in HW	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature life test function for sensors, symmetric algorithms (e.g. DES / AES)	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature life test function for sensors, symmetric algorithms (e.g. DES / AES)	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature life test function for sensors	Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature life test function for sensors, symmetric algorithms (e.g. DES / AES), triple DES in HW, elliptic curves in HW
2 x 16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART	2 x 16-bit autoreload timer, PLL, interrupt, CRC, RNG, UART	CRC, UART, RNG, 2x 16 bit timer, interrupt, DES	CRC, UART, RNG, 2 x 16 bit timer, interrupt, DES	CRC, UART, RNG, 2 x 16 bit timer, interrupt, DES	CRC, UART, RNG, 2x 16 bit timer, interrupt, DES
Module M5, die	Module M5, die	Module M5, die	Module M5, die	Module M5, die	Module M5, die
Banking (debit – credit), E-Purse, Pay-TV, M/E-commerce, security access, GSM, USIM, Ext. healthcare, multifunctional card	Healthcare, Basic GSM, R-UIM, identification, transport	Payment, GSM, Pay-TV, security access identification, digital signatures	GSM	GSM	Payment, GSM, Pay-TV, identification, digital signatures, multiapplication card based on MultOS, Java or WinSC multifunctional card
Planned: CC EAL 5+ Visa, CAST, Proton, MultOS, ZKA	–	Planned: CC EAL 5+, Visa, CAST, Proton, MultOS, ZKA	–	–	Planned: CC EAL 5+, Visa, CAST, Proton, MultOS, ZKA

Contactless Memories

Dual Interface / Contactless Controllers

Components for Contactless Terminals

Contact-Based Security Memories

Security Controller Overview in EEPROM Sizes

## Security Controller Overview in EEPROM Sizes

≥ 72 KB			
SLE 66CX1360PE	SLE 88CX720P	SLE 88CFX4000P	SLE 88CFX4002P
Security cryptocontroller	Security cryptocontroller	Security cryptocontroller	Security Cryptocontroller
246 kByte	240 kByte	80 kByte	240 kByte
136 kByte	80 kByte	400 kByte	400 kByte
7,100 Byte	8 kByte + 700 Byte crypto	16 kByte + 880 Byte Crypto	16 kByte + 880 Byte Crypto
8 / 16 bit	32 bit	32 bit	32 bit
Yes	1,100 bit arithmetic	1,408 bit arithmetic	1,408 bit arithmetic
1 to 33 MHz	Up to 55 MHz	Up to 66 MHz	Up to 66 MHz
1 to 7.5 MHz	1 to 10 MHz	1 to 10 MHz	1 to 10 MHz
1.8 V, 3.0 V, 5.0 V	2.7 V–5.5 V	1.62 V–5.5 V	1.62 V–5.5 V
10 mA	2.5 mA	1.75 mA	1.75 mA
100 µA (100 µA)	100 µA (100 µA)	100 µA (100 µA)	100 µA (100 µA)
–25 to +70° (85°)	–25 to +85°	–25 to +85°	–25 to +85°
< 2.9 ms	4.1 ms (typ.)	2.3 ms (typ.)	2.3 ms (typ.)
1 to 64 Byte	1 to 64 Byte	1 to 128 Byte	1 to 128 Byte
Yes	Yes	Yes	Yes
Tamper-proof design, chip ID, counter measures against SEMA / DEMA, SPA / DPA, DFA and timing attacks, sensor concept: low and high voltage sensors, frequency sensors and filters, light-, glitch-, temperature life test function for sensors, symmetric algorithms (e.g. DES / AES), triple DES in HW, elliptic curves in HW	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, triple DES in HW, elliptic curves in HW, asymmetric algorithms, hardware-supported (e.g. RSA), bus encryption, dual rail logic	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, triple DES in HW, elliptic curves in HW, asymmetric algorithms, hardware-supported (e.g. RSA), bus encryption, dual rail logic	Tamper-proof design, chip ID, counter measures against reverse engineering, SPA / DPA, DFA / EMA, memory encryption, sensor concept: voltage-, frequency-, light-, temperature-, glitch sensor, active shield, triple DES in HW, elliptic curves in HW, asymmetric algorithms, hardware-supported (e.g. RSA), bus encryption, dual rail logic
CRC, UART, RNG, 2x 16 bit timer, ACE, interrupt, DES	3x 16-bit autoreload timer, PLL, interrupt, RNG, UART, triple DES accelerator	3x 16-bit autoreload timer, PLL, interrupt, RNG, UART, triple DES accelerator	3x 16-bit autoreload timer, PLL, interrupt, RNG, UART, triple DES accelerator
Module M5, die	Module M5, die	Module M5, die	Module M5, die
Payment, GSM, Pay-TV, identification, digital signatures, multiapplication card based on MultOS, Java or WinSC	Java-based card, banking (e-purse), Pay-TV, PKI, security access, GSM / USIM, multifunctional card	Java-based card, banking (e-purse), Pay-TV, PKI, security access, GSM / USIM, multifunctional card	Java-based card, banking (e-purse), Pay-TV, PKI, security access, GSM / USIM, multifunctional card
Planned: CC EAL 5+, Visa, CAST, Proton, MultOS, ZKA	CC EAL 5+, CAST	Planned: CC EAL 5+, CAST	Planned: CC EAL 5+, CAST

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